



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/676,149	10/02/2003	Shigeki Aoki	106368.01	6383

25944 7590 11/17/2005

OLIFF & BERRIDGE, PLC
P.O. BOX 19928
ALEXANDRIA, VA 22320

EXAMINER

DINH, DUC Q

ART UNIT	PAPER NUMBER
----------	--------------

2674

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary**Application No.**

10/676,149

Applicant(s)

AOKI, SHIGEKI

Examiner

DUC Q. DINH

Art Unit

2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 02 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 3/5/04, 7/11/05
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claim 1 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claim 1 recites the limitation “the MPU send an ID monitor command to the driver IC when the MUP reads an ID from the ID setting circuit”. Although the specification page 9 lines 8-10 does disclose the MPU send an ID monitor command to the driver IC when the MPU reads an ID from the ID setting circuit”, the specification is not enabling as how to one of ordinary skill in the art would actually read the ID from the ID setting circuit. In addition, the specification page 22, lines 25-28 only teaches “to read the ID of the liquid crystal display driver IC 10, the test command is input from the MPU 300 to the liquid crystal display”. Therefore, the examiner examines the application as best understanding the claim language

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

Art Unit: 2674

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Furuhashi et al. (JP-2000-148080).

In reference to claim 1, Furuhashi discloses a display unit (Fig. 11) comprising:

a display section 1526;

a display driver IC (1518) among a plurality of types of display driver ICs capable of driving and controlling the display section (interface 1518-1521); and

an MPU (1501) sending a command and display data to the display driver IC [0083], wherein the display driver IC (901; Fig. 10) comprises:

an ID setting circuit (129) in Fig.10, [0083] ; and

a command decoder (128) Fig. 10, [0083] decoding the command from the MPU (1501), wherein the MPU (1501) has a storage section (1053) for storing a plurality of types of control contents for a plurality of types of display driver ICs (1518-1521) including the display driver IC (1518); [0108-0111],

wherein the MPU (1501) sends an ID monitor command to the display driver IC [0085] when the MPU reads an ID from the ID setting circuit (129), wherein the display driver IC (1518) sends ID information from the ID setting circuit to the MPU in accordance with a signal generated by the command decoder decoding the ID monitor command [0113-0114], and

wherein the MPU reads the ID information so as to control the display driver IC (1518) among a plurality of types of display driver ICs (1518-1521) according to the corresponding control contents in the storage section among the plurality of types of control contents for a plurality of types of display driver ICs including the display driver IC [0086-0087].

Conclusion


5. The prior arts made of record and not relied upon is considered pertinent to applicant's disclosure. See Form PTO 892.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to DUC Q DINH whose telephone number is (571) 272-7686. The examiner can normally be reached on Mon-Fri from 8:00.AM-4:00.PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edouard Patrick can be reached on (571) 272-7603. The fax phone number for the organization where this application or proceeding is assigned is **571-273-8300**.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DUC Q DINH
Examiner
Art Unit 2674



PATRICK N. EDOUARD
SUPERVISORY PATENT EXAMINER

DQD
November 8, 2005